Fig. 10

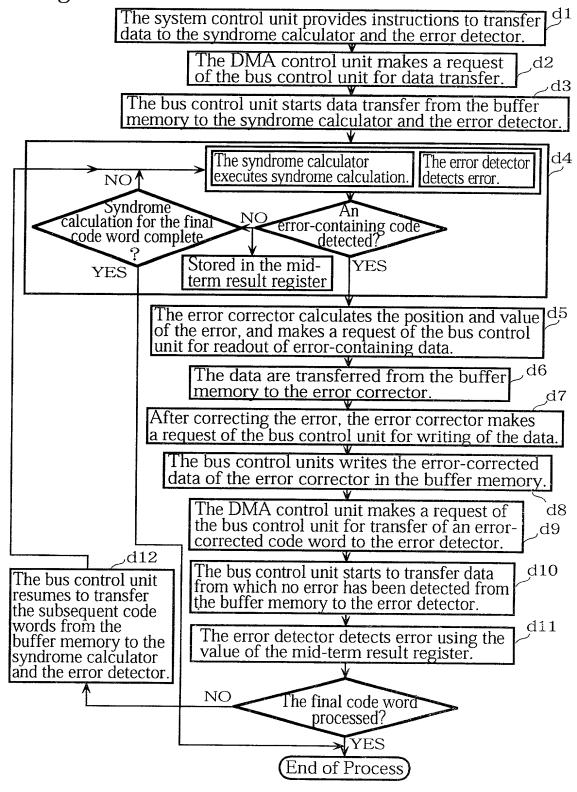


Fig. 11 $\sqrt{10}$ Upstream Process Unit 42 41 First Buffer Memory Second Buffer Memory Overwriting and Reading Overwriting and Reading Unit 5,7 Syndrome Calculator, Error Detector and the Like Downstream Process Unit **Buffer Switch Control** Buffer Data Transfer Control Unit Unit Flag Switch Unit Flag 101 102 Initial Setting Unit 1 103 System Control Unit

